

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1

Complete if Known		
Application Number	10/672,697	
Filing Date	September 26, 2003	
First Named Inventor	Eshwari P. Komarla	
Art Unit		
Examiner Name		
Attorney Docket Number	42P16549	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the stricle (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T,
A	_	Intel, "Itanium Processor Family System Abstraction Layer," 11/2002, pgs. i-B14, Intel, USA	
			1
Examiner Signature		Date Considered 825 07	

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450